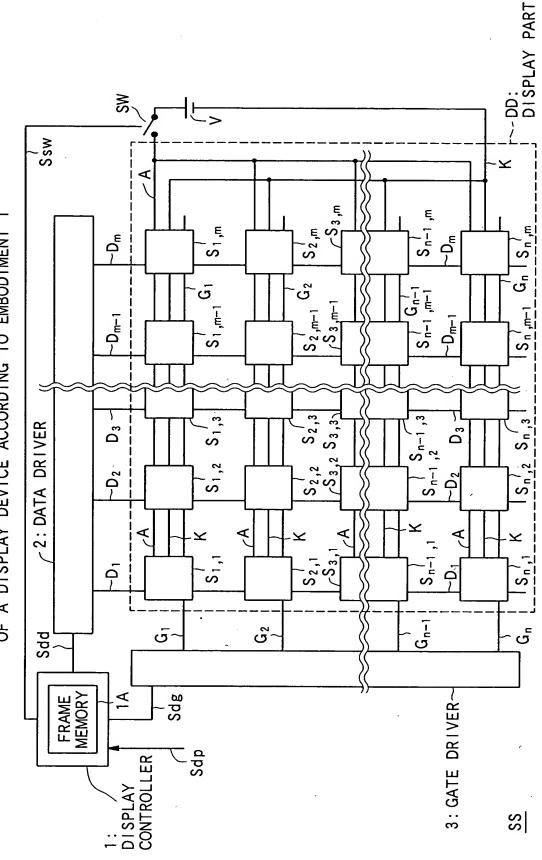
FIG. T

BLOCK DIAGRAM SHOWING THE SCHEMATIC CONFIGURATION
OF A DISPLAY DEVICE ACCORDING TO EMBODIMENT 1



Shinji KURIKI Q78438 DISPLAY CONTROLLER, DISPLAY SYSTEM..... Darryl Mexic 202-293-7060 November 13, 2003 2 of 7

FIG. 2

BLOCK DIAGRAM SHOWING THE DETAILED CONFIGURATION OF A DISPLAY DEVICE ACCORDING TO EMBODIMENT 1

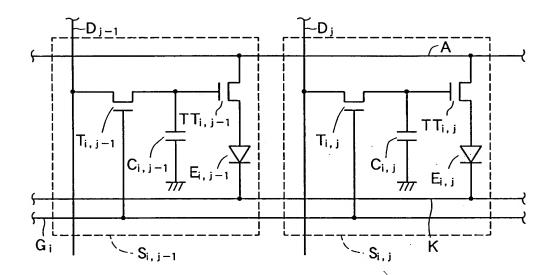
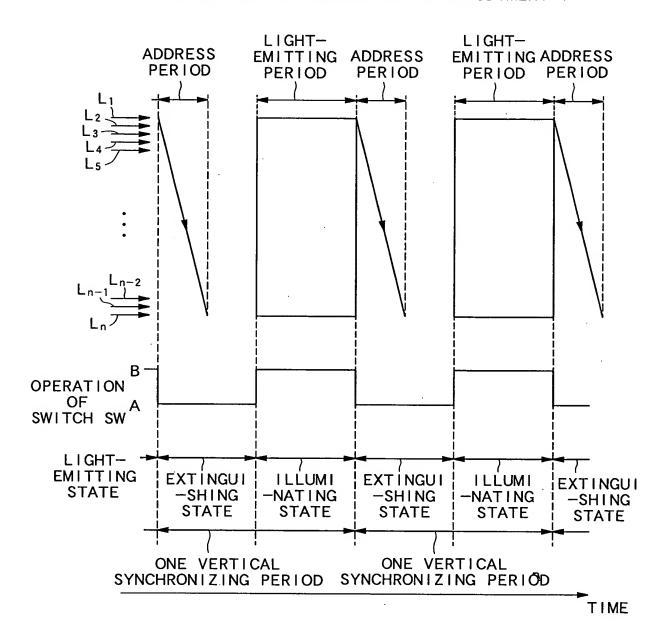


FIG. 3

TIMING CHART SHOWING A DRIVING STATE OF A DISPLAY DEVICE ACCORDING TO EMBODIMENT 1



S_{n-1},m

S -1 ,m-1

S_{n-1},3

S_n-1

S_{n-1}, 1

 G_{n-1}

3: GATE DRIVER~

ر س

ر اسام_

> . ص

~-DD: DISPLAY PART

Sn,m

_ ნ

Sn'm-1

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င္ပ

SS1

BLOCK DIAGRAM SHOWING THE SCHEMATIC CONFIGURATION OF A DISPLAY DEVICE ACCORDING TO EMBODIMENT 2 S 3,m S1,m , S2,m س_ ~ S₂,m-1 G₂ /S_{3,m-1} 5 S1,m-1 -D_{m-1} FIG. 4 \$2,3 $S_{3,3}$ 51,31 ن ص -2:DATA DRIVER S2,2 S3.2 S_{1,2} $^{-}$ D₂ 51,1 ر 13. 1,22,1 <u>-</u> $\frac{2}{5}$ 5 Sdd 10A FRAME MEMORY Sdg

10: / LA DISPLAY CONTROLLER

Sdp

FIG. 5

TIMING CHART (I) SHOWING A DRIVING STATE OF A DISPLAY DEVICE ACCORDING TO EMBODIMENT 2

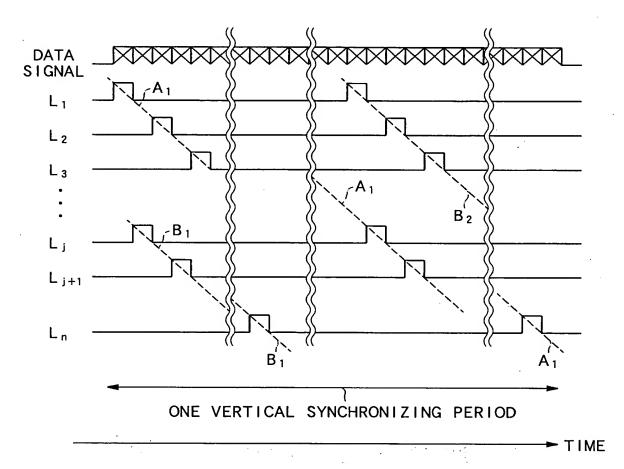


FIG. 6

TIMING CHART (II) SHOWING A DRIVING STATE OF A DISPLAY DEVICE ACCORDING TO EMBODIMENT 2

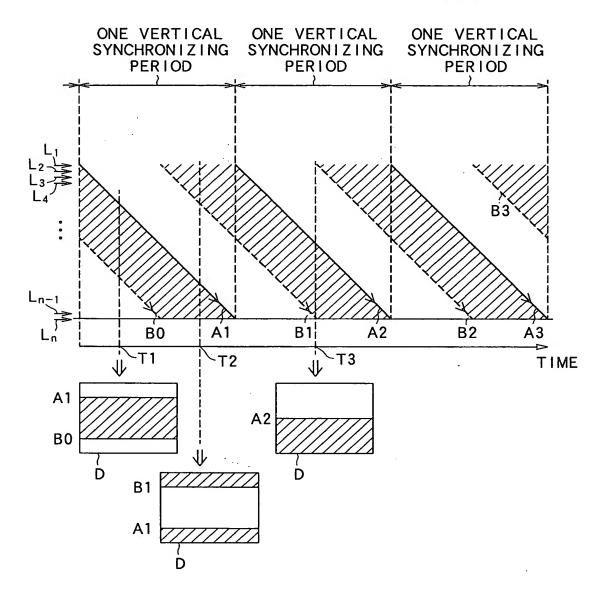


FIG. 7

